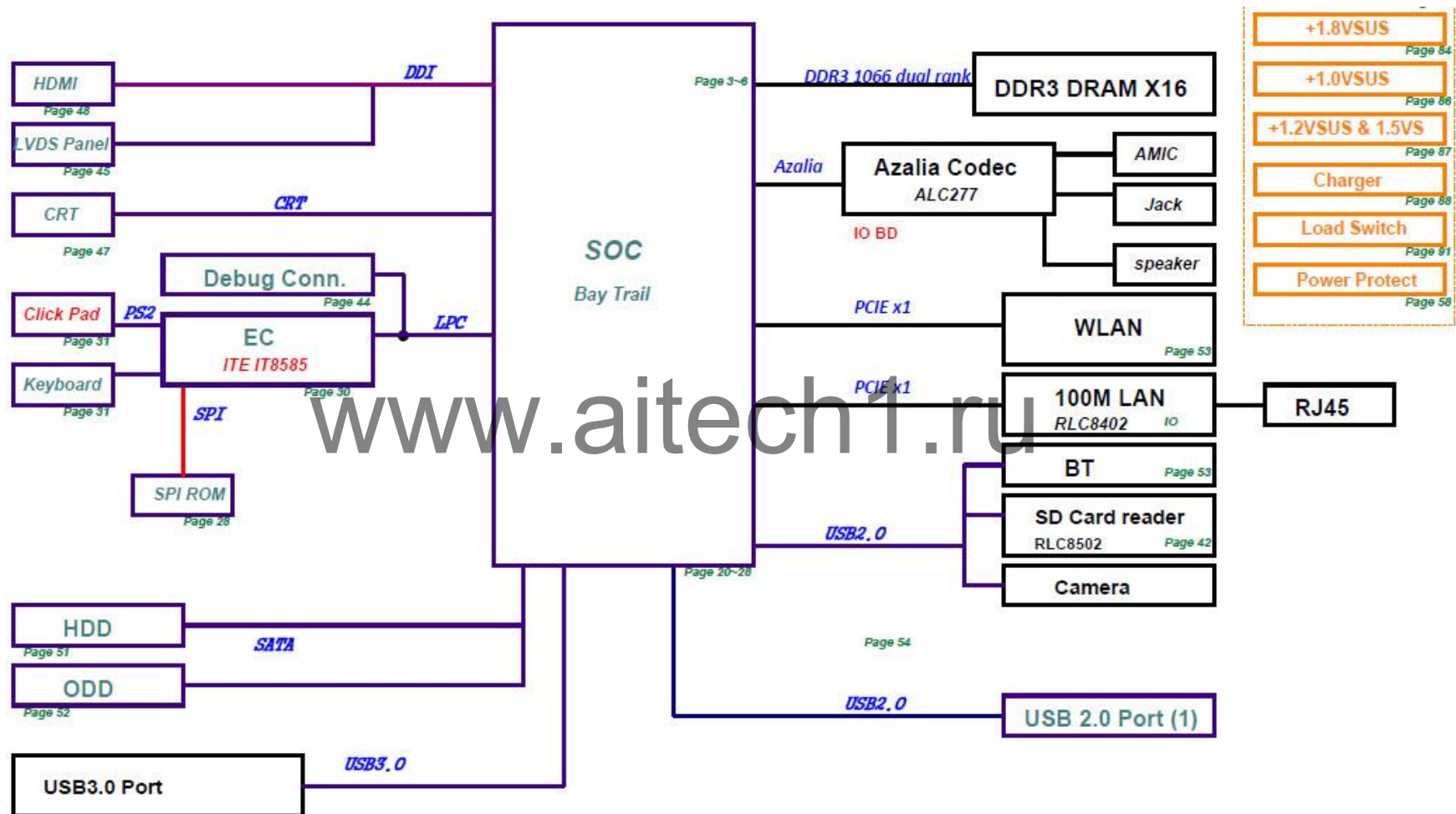
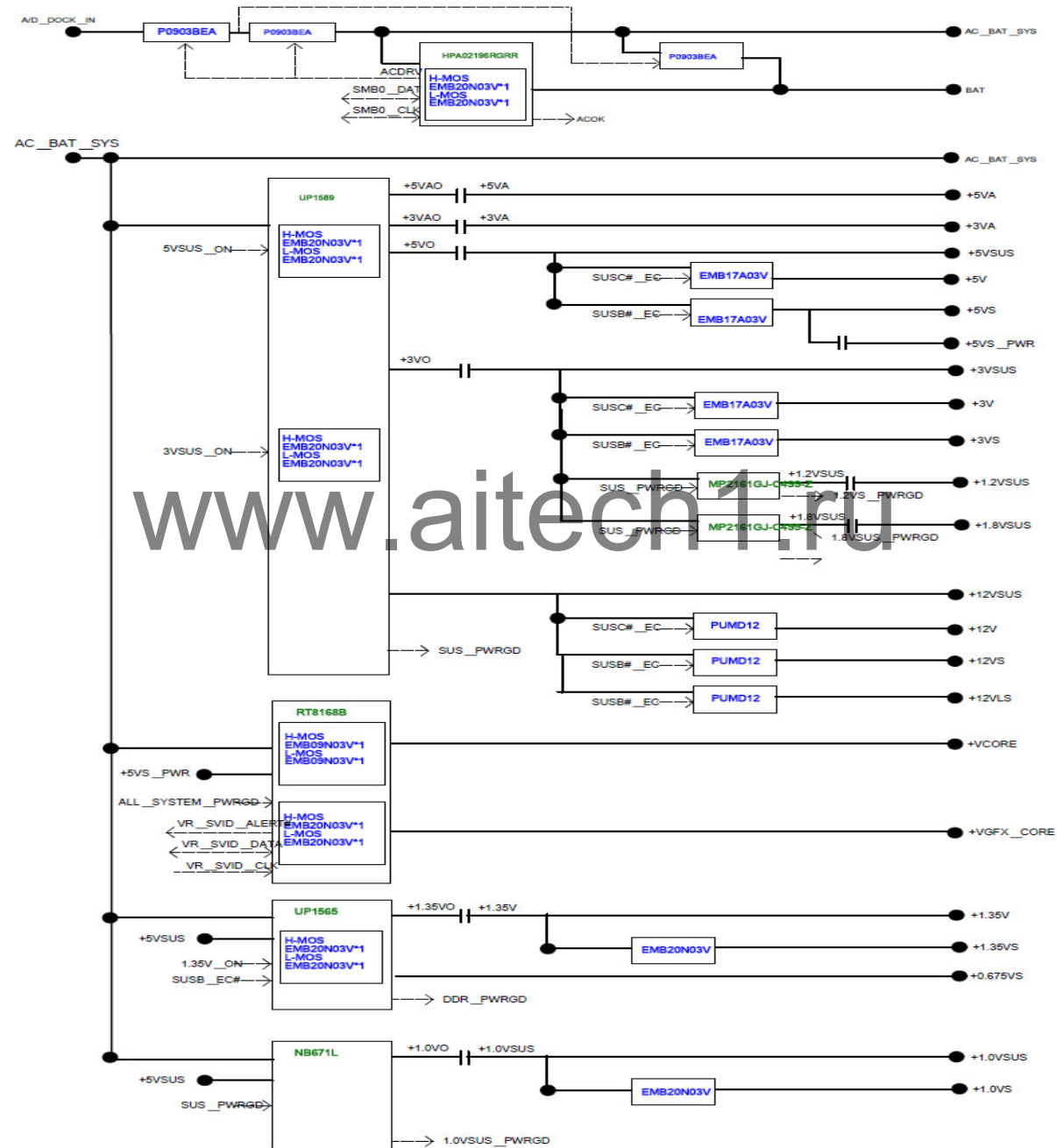


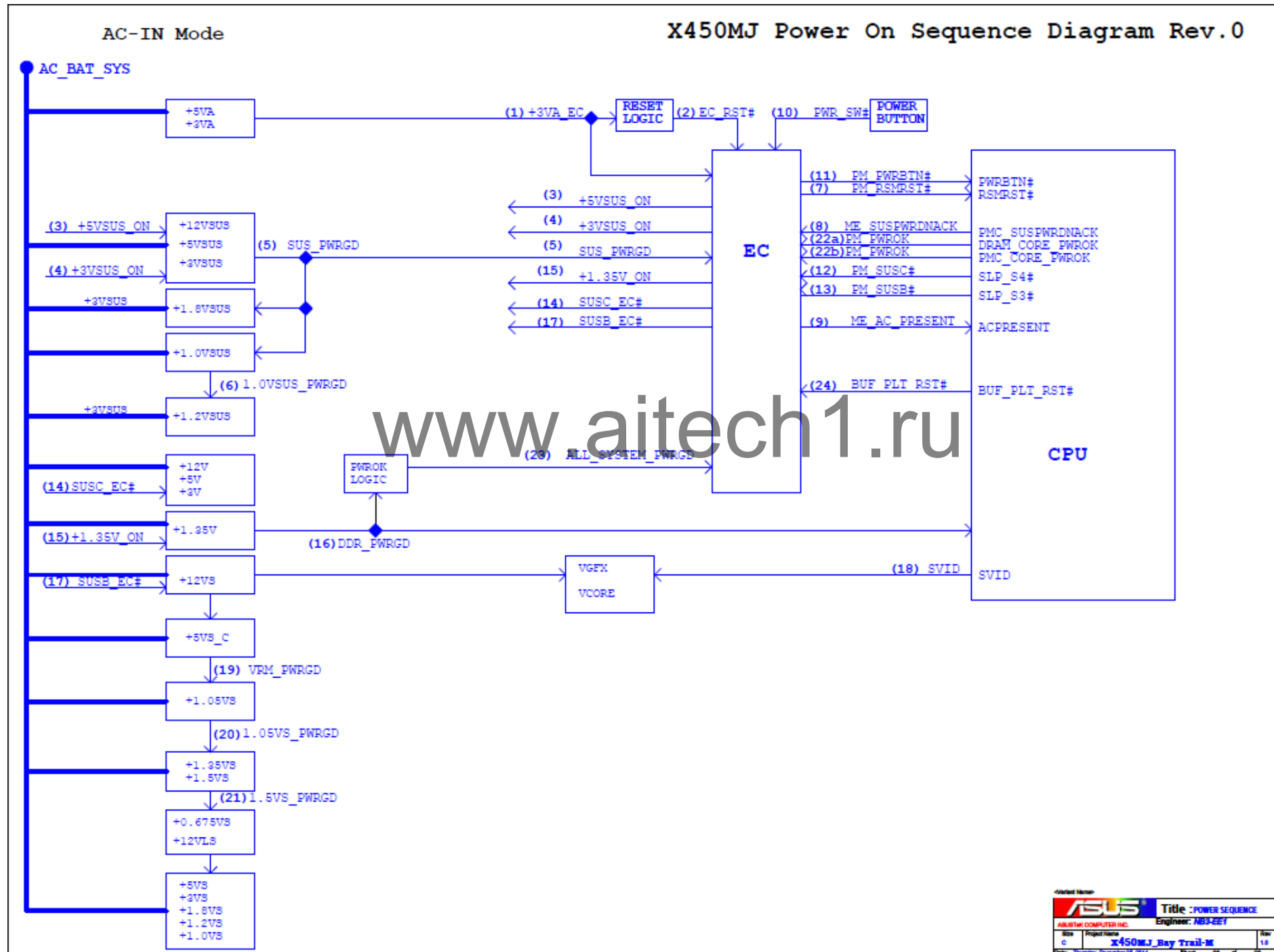
## BLOCK DIAGRAM



# POWER FLOW



# POWER ON SEQUENCE

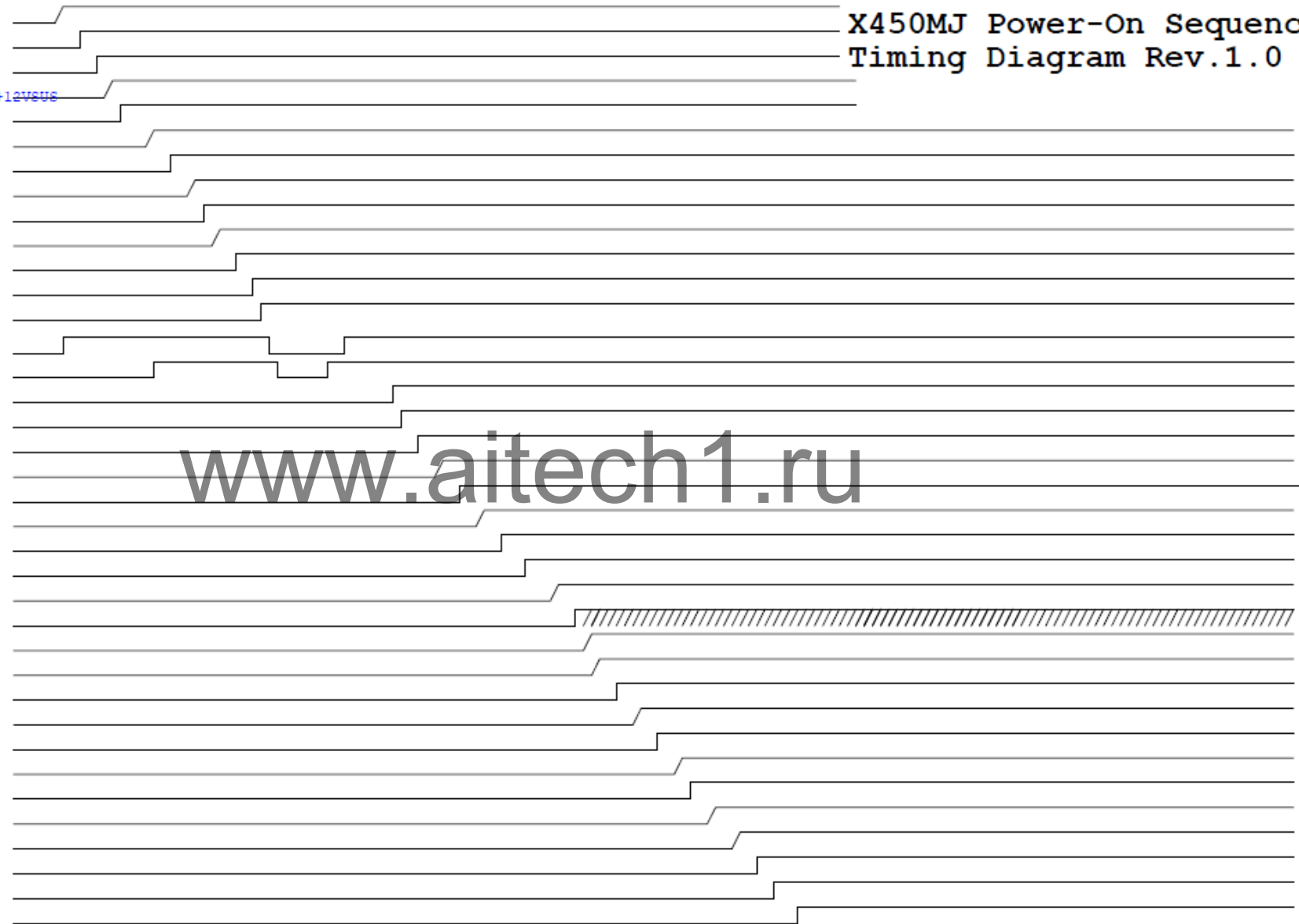


# AC\_IN POWER ON SEQUENCE

AC-IN  
Mode

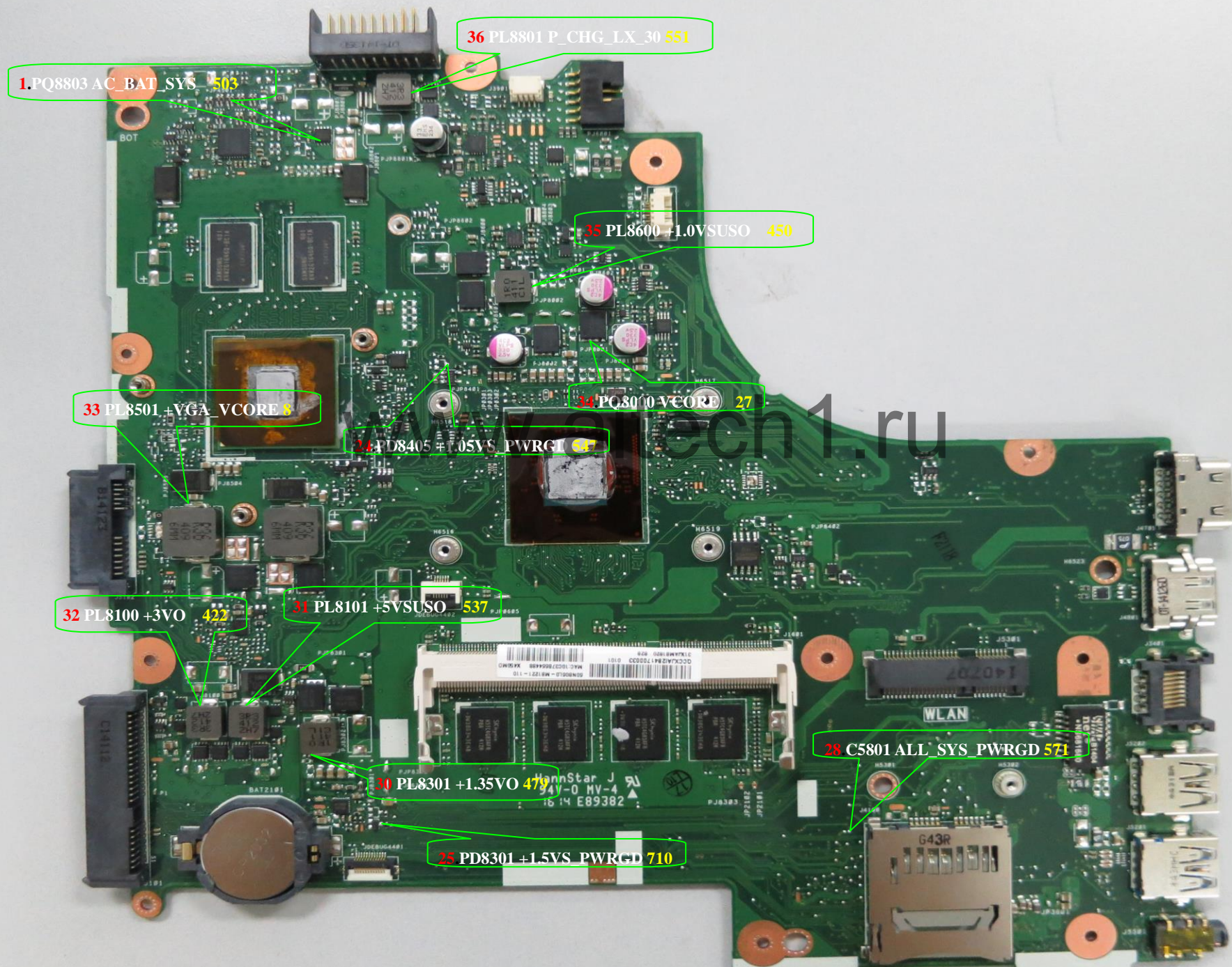
X450MJ Power-On Sequence  
Timing Diagram Rev.1.0

1 +3VA/+5VA/+3VA\_EC  
2 EC\_RST#  
3 +5VSUS\_ON  
+5VSUS/+12VSUS  
4 +3VSUS\_ON  
+3VSUS  
5 SUS\_PWRGD  
+1.8VSUS/+1.0VSUS  
6 1.0SUS\_PWRGD  
+1.2VSUS  
7 PM\_RSMRST#  
8 ME\_SusPwrDnAck  
9 ME\_AC\_PRESENT  
10 PWR\_SW#  
11 PM\_PWRBTN#  
12 PM\_SUSC#  
13 PM\_SUSB#  
14 SUSC\_EC#  
+12V/+5V/+3V  
15 +1.35V\_ON  
+1.35V  
16 DDR\_PWRGD  
17 SUSB\_EC#  
+12VS  
18 SVID  
VFGX/VCORE  
+5VS\_C  
19 VRM\_PWRGD  
+1.05VS  
20 +1.05VS\_PWRGD  
+1.35VS/+1.5VS  
21 +1.5VS\_PWRGD  
+0.675VS/+12VLS  
+5VS/+3VS/+1.8VS/+1.2VS/+1.0VS  
22 PM\_PWROK  
23 ALL\_SYSTEM\_PWRGD  
24 BUF\_PLT\_RST#





## Signal Measure Point-Bottom





## Signal Measure Point-Top

